

Item # ADC-207LM-QL-C, CMOS Flash A/D

List Price

CMOS Flash A/D



Image is for illustration purposes only

The ADC-207 is a 7 Bit flash converter using an advanced high-speed VLSI 1.2 micron CMOS process. This process offers some very distinctive advantages over other processes, making the ADC-207 unique. The smaller geometrics of the process achieve high speed, better linearity and superior temperature performance.

SPECIFICATIONS

Description	7 Bit, 20MHz (20MSPS), CMOS Flash Analog to Digital (A/D) Converter, 0 to +5V Input range, -55°C to +125°C temperature range, LCC package, High Reliability Screening, RoHS compliant.
Resolution	7 bits
Number of Channels	1
Sampling Rate	20 MHz
Power Consumption	0.25 W
Differential Non-Linearity Error/Other	0.3 LSB
Integral Non-Linearity Error/Other	0.8 LSB
Package Type	CLCC
Input Range 1st (min)	0 V
Input Range 1st (max)	5 V
Required Supply Voltage 1st	5 V
Total Harmonic Distortion	-40 dB
Operating Temp. Range (min)	-55 °C
Operating Temp. Range (max)	125 °C
RoHS	Yes

Status	Recommended for new design
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