

Item # ADC-208AMM, CMOS Flash A/D

List Price

CMOS Flash A/D


Image is for illustration purposes only

The ADC-208A utilizes an advanced VLSI 1.2 micron CMOS in providing 20MHz sampling rates at 8 Bits. The flexibility of the design architecture and process delivers latch-up free operation without external components and operation over the full military range.

SPECIFICATIONS

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| Description | 8 Bit, 20MHz (20MSPS), CMOS Flash Analog to Digital (A/D) Converter, 0 to +5V Input range, -55°C to +125°C temperature range. |
| Resolution | 8 bits |
| Number of Channels | 1 |
| Sampling Rate | 20 MHz |
| Power Consumption | 0.25 W |
| Differential Non-Linearity Error/Other | 0.5 LSB |
| Integral Non-Linearity Error/Other | 0.5 LSB |
| Package Type | DDIP |
| Input Range 1st (min) | 0 V |
| Input Range 1st (max) | 5 V |
| Required Supply Voltage 1st | 5 V |
| Total Harmonic Distortion | -46 dB |
| Operating Temp. Range (min) | -55 °C |
| Operating Temp. Range (max) | 125 °C |
| RoHS | No |

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| Status | Recommended for new design |
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