

Item # ADC-228AMC, CMOS Flash A/D

List Price

CMOS Flash A/D


The ADC-228A combines analog front-end circuitry and a flash A/D converter to digitize high-speed analog signals at a rate of 20 million samples per second. The ADC-228A contains an 8 Bit, 20MHz, flash A/D, a wideband analog input buffer, a precision voltage reference, temperature compensation circuitry, reference trims, and a three-state output buffer in a 24-pin package.

Image is for illustration purposes only

SPECIFICATIONS

Description	8 Bit, 20MHz (20MSPS), CMOS Complete Flash Analog to Digital (A/D) Converter, 0 to +5V Input range, 0°C to +70°C temperature range.
Resolution	8 bits
Number of Channels	1
Sampling Rate	20 MHz
Power Consumption	0.9 W
Differential Non-Linearity Error/Other	0.5 LSB
Integral Non-Linearity Error/Other	0.5 LSB
Package Type	DDIP
Input Range 1st (min)	0 V
Input Range 1st (max)	5 V
Required Supply Voltage 1st	5 V
Required Supply Voltage 3rd	15 V
Required Supply Voltage 4th	-15 V
Total Harmonic Distortion	-56 dB
Signal to Noise Ratio	45 dB

Analog Bandwidth	40 MHz
Operating Temp. Range (min)	0 °C
Operating Temp. Range (max)	70 °C
RoHS	No
Status	Recommended for new design