

Item # DAC-HZ12BMM-QL, Digital-to-Analog Converters

List Price

Digital-to-Analog Converters

12 Bit, 300nS Iout Settling, 3uS Vout Settling, Digital to Analog Converter, 0 to -2mA, ±1mA Iout, ±2.5V, ±5V, 0 to +5V, or 0 to +10V Vout range. Bin., Comp. Bin., Comp. Offset Bin. Out Coding, -55°C to +125°C temp. range. High Rel. Screening.

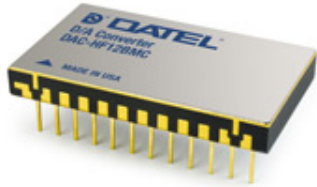


Image is for illustration purposes only

SPECIFICATIONS

Resolution	12 bits
Number of Channels	1
Settling Time	3000 ns
Power Consumption	0.39 W
Differential Non-Linearity Error/Other	0.25 LSB
Integral Non-Linearity Error/Other	0.25 LSB
Package Type	DDIP
Required Supply Voltage 3rd	15 V
Required Supply Voltage 4th	-15 V
Operating Temp. Range (min)	-55 °C
Operating Temp. Range (max)	125 °C
RoHS	No
Status	Recommended for new design